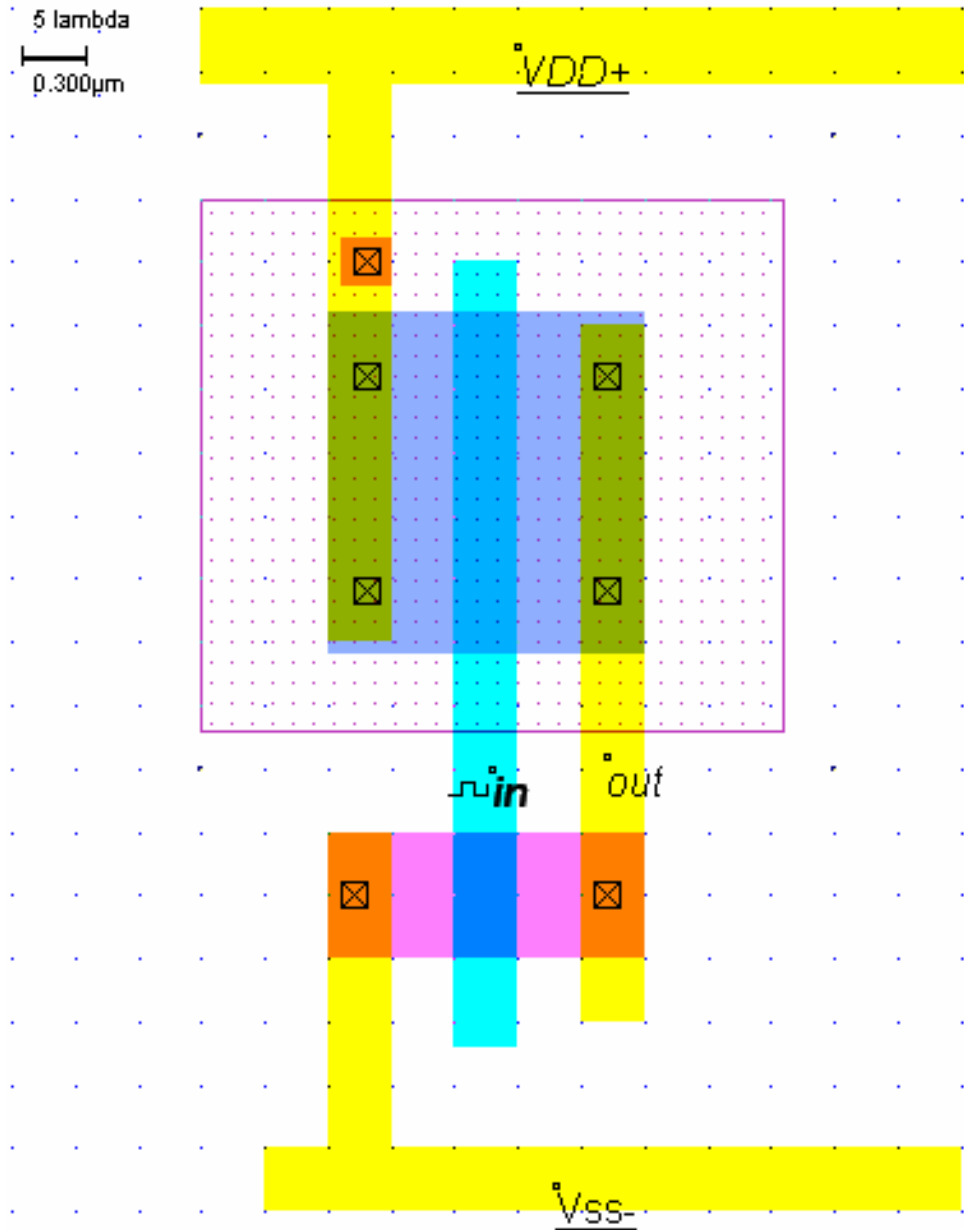




Subject : VLSI Design

Assignment 1: Effect of W_p on t_{phl} and t_{plh}



Layout Name:

1	Lambda Rule	$\lambda = 0.3 \mu\text{m}$
2	Area	513 (μm^2)
3	W_p	25 (μm)
4	W_n	10 (μm)

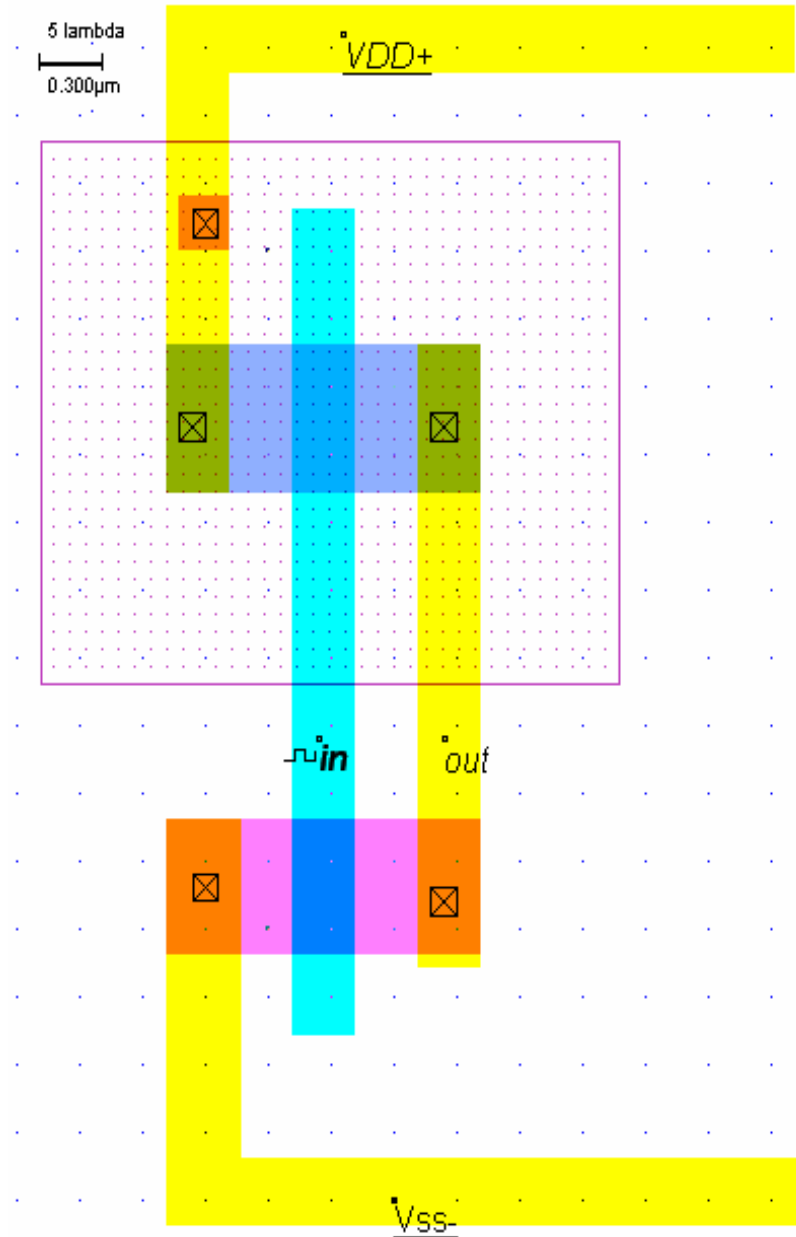
LAYOUT ($W_p = 2.5W_n$)

P D Khandekar



Subject : VLSI Design

Assignment 1: Effect of W_p on t_{pHL} and t_{pLH}



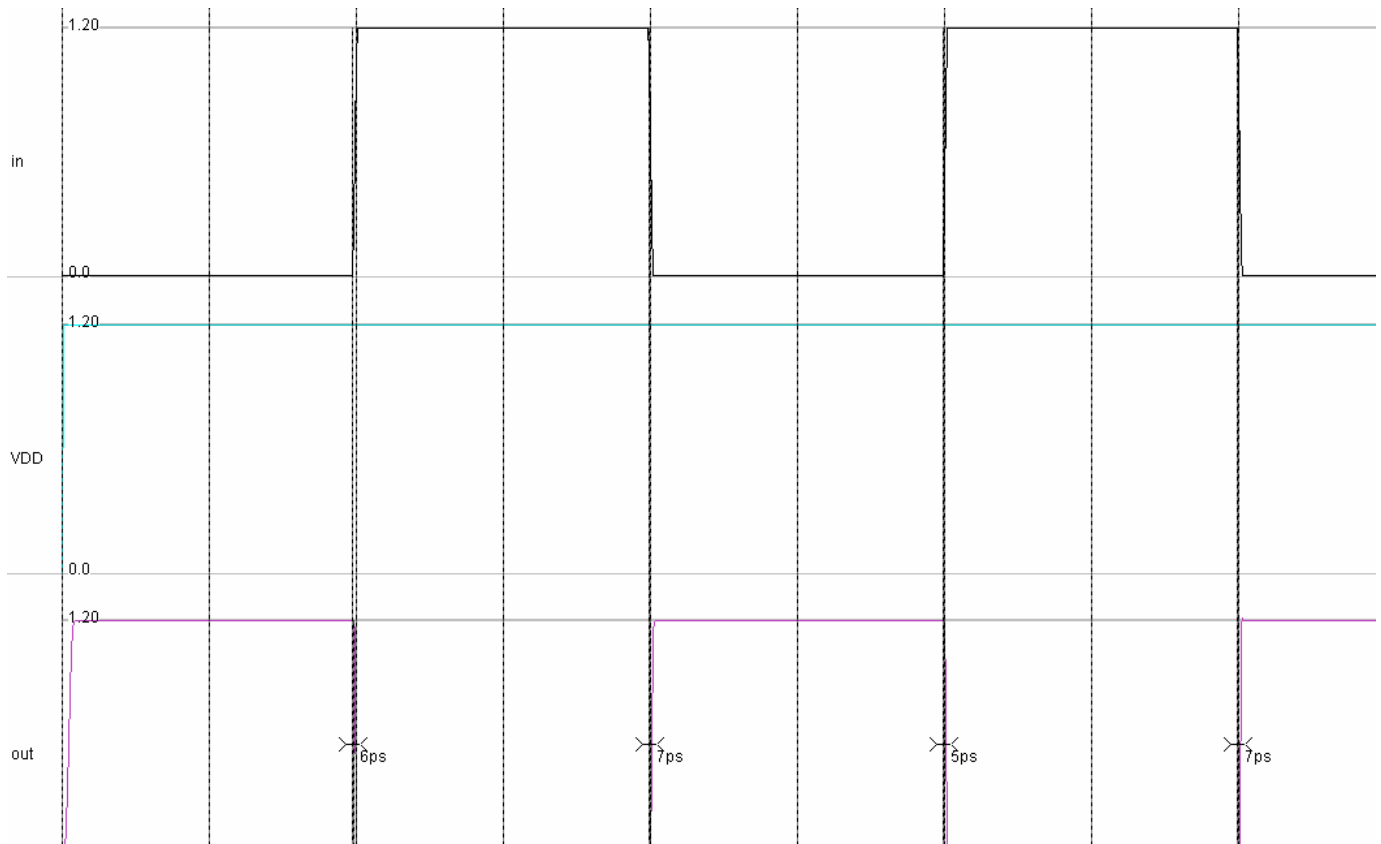
Layout Name:		
1	Lambda Rule	$\lambda = 0.3 \mu\text{m}$
2	Area	432 (μm^2)
3	W_p	10 (μm)
4	W_n	10 (μm)
P D Khandekar		

LAYOUT ($W_p = W_n$)



Subject : VLSI Design

Assignment 1: Effect of W_p on t_{pHL} and t_{pLH}



FUNCTIONAL SIMULATION
($W_p=2.5W_n$)

Design Name:

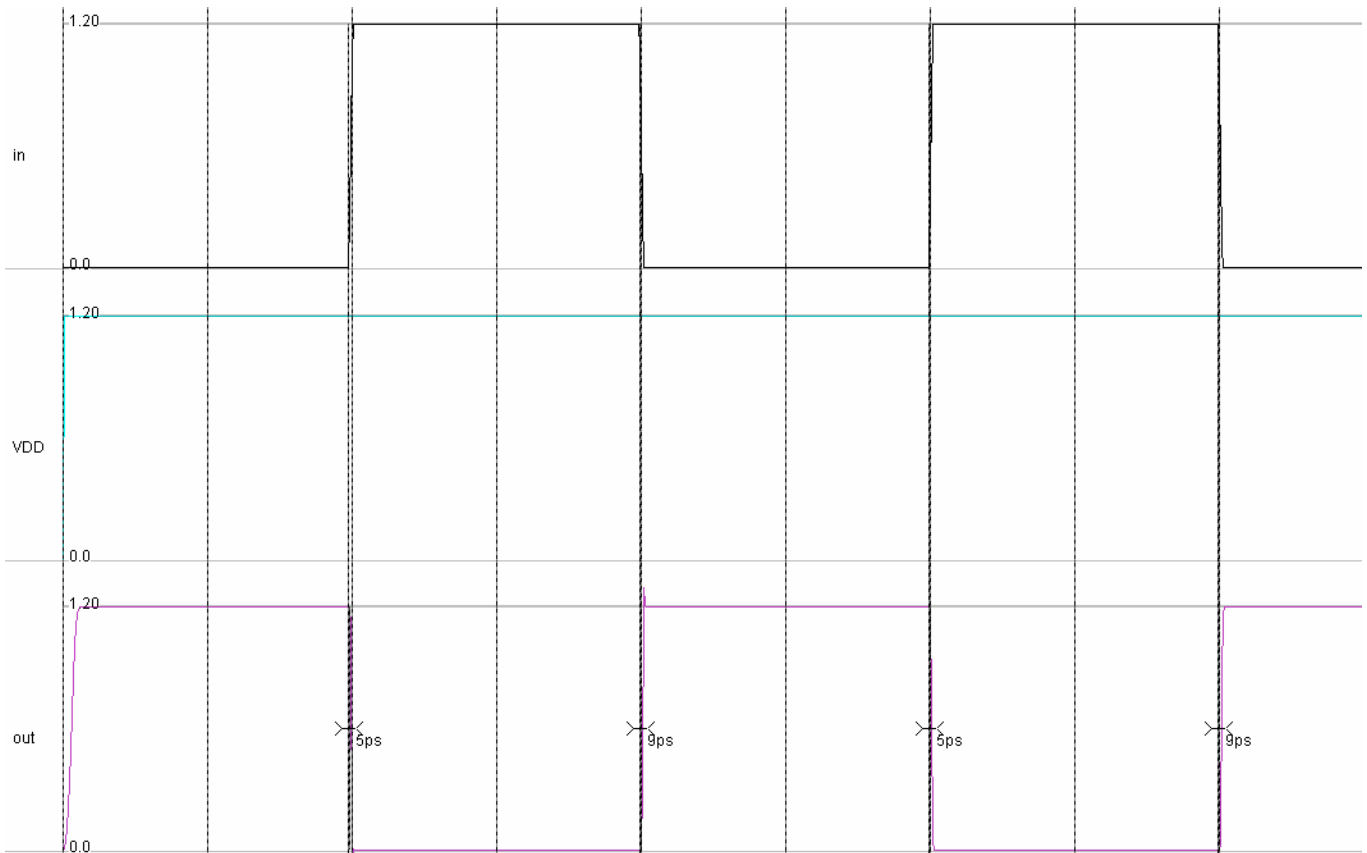
1	Power Dissipation	0.816(μ W)
2.	Delay	(s)
3.	T_{pHL}	7(ps)
4	T_{pLH}	5(ps)

P D Khandekar



Subject : VLSI Design

Assignment 1: Effect of W_p on t_{phl} and t_{plh}



**FUNCTIONAL SIMULATION
($W_p=W_n$)**

Design Name:		
1	Power Dissipation	0.440(μ W)
2.	Delay	(s)
3.	Tphl	9(ps)
4	Tplh	5(ps)

P D Khandekar