



## VIJAYA NAVNATH AHER

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, Department of Electronics and Telecommunications

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<b>Education:</b>	<ul style="list-style-type: none"><li>• Pursuing PhD (Thapar University,patiala)</li><li>• B.E. ( Electronics) Pune University, 2000</li><li>• M.E ( Electronics) Dr. B.A.M.U. Aurangabad, JNEC ,2004</li><li>• Total teaching Experience : 8 years</li></ul>
<b>Research Interests:</b>	<ul style="list-style-type: none"><li>• Advanced instrumentation and control system</li><li>• Power electronics</li></ul>
<b>Funded Research Projects:</b>	-
<b>Teaching:</b>	<ul style="list-style-type: none"><li>• Digital Electronics</li><li>• Analog communication</li><li>• Semiconductor devices and circuits</li><li>• Analog integrated circuits</li><li>• Basic electronics engineering</li><li>• Network theory</li><li>• Electronics circuits and applications</li><li>• Mechatronics</li><li>• Power electronics-I</li></ul>
<b>Projects:</b>	<ul style="list-style-type: none"><li>• Undergraduate Projects guided: 10+</li></ul>
<b>Professional:</b>	-AM of IETE
<b>Publications:</b>	<ul style="list-style-type: none"><li>• International Conferences: 03</li><li>• National Conferences : 05</li><li>• Book s : Nil</li></ul>
<b>Computer Skills:</b>	<ul style="list-style-type: none"><li>• Languages: Matlab, C, multisim</li><li>• Applications: Microsoft office, common Windows database,</li><li>• Operating Systems: Windows XP</li></ul>